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TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

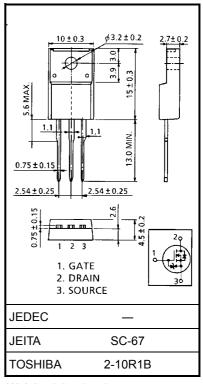
2SK2952

Chopper Regulator Applications

- Low drain-source ON resistance $R_{DS}(ON) = 0.4 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 8.0 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 400 \ V)$
- Enhancement-mode : $V_{th} = 2.0 \sim 4.0 V (V_{DS} = 10 V, I_D = 1 mA)$

Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	400	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	400	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	8.5	А	
	Pulse (Note 1)	I _{DP}	34	А	
Drain power dissipation	n (Tc = 25°C)	PD	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	427	mJ	
Avalanche current		I _{AR}	8.5	А	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch−c)}	3.125	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	62.5	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C, L = 9.6 mH, R_G = 25 Ω , I_{AR} = 8.5 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution. Unit: mm

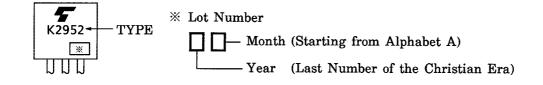
Electrical Characteristics (Ta = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	_		±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I_{G} = ±10 µA, V_{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 400 V, V _{GS} = 0 V	_	-	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	400	-	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0		4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 5 A		0.4	0.55	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	4.0	8.0	_	S
Input capacitance	e	C _{iss}			1340	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		160	_	
Output capacitance		Coss			490		
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{_{0 \text{ V}}} \int_{\mathcal{C}} \stackrel{I_{D} = 5 \text{ A}}{_{V \text{ OUT}}} V_{OUT}$ $R_{L} = 40 \Omega$ $V_{DD} = 200 \text{ V}$	_	22	_	
	Turn-on time	t _{on}		_	60	_	20
	Fall time	t _f		_	32	_	ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w = 10 µs	_	140	-	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 320 V, V _{GS} = 10 V, I _D = 8.5 A	_	34	_	nC
Gate-source charge		Q _{gs}		_	18	_	
Gate-drain ("miller") Charge		Q _{gd}][16	_	

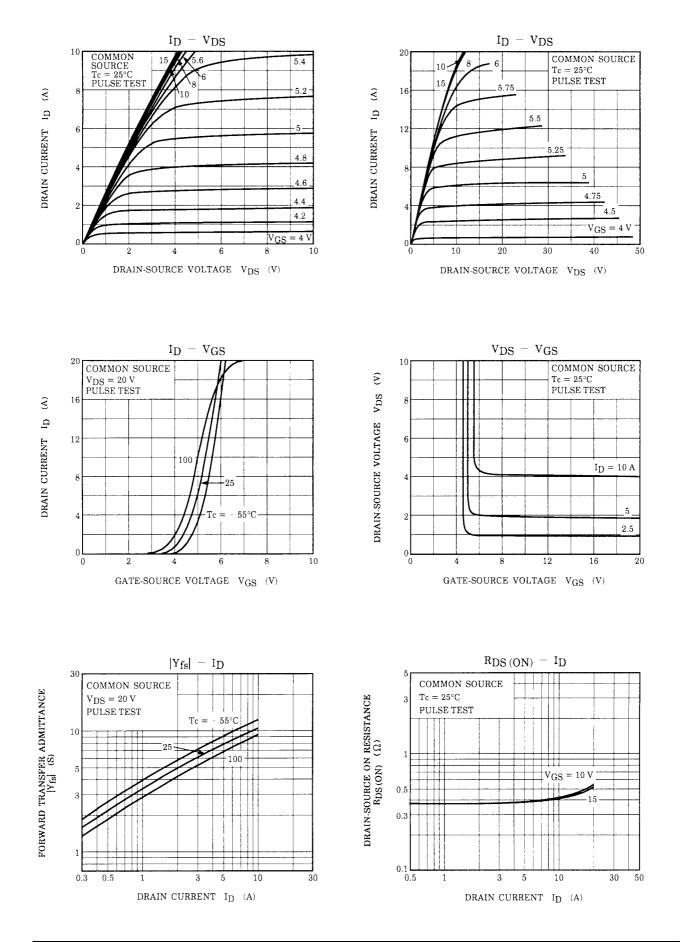
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	-	_	_	8.5	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	34	A
Forward voltage (diode)	V _{DSF}	I _{DR} = 8.5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 8.5 A, V _{GS} = 0 V	_	350	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/µs	_	2.6	_	μC

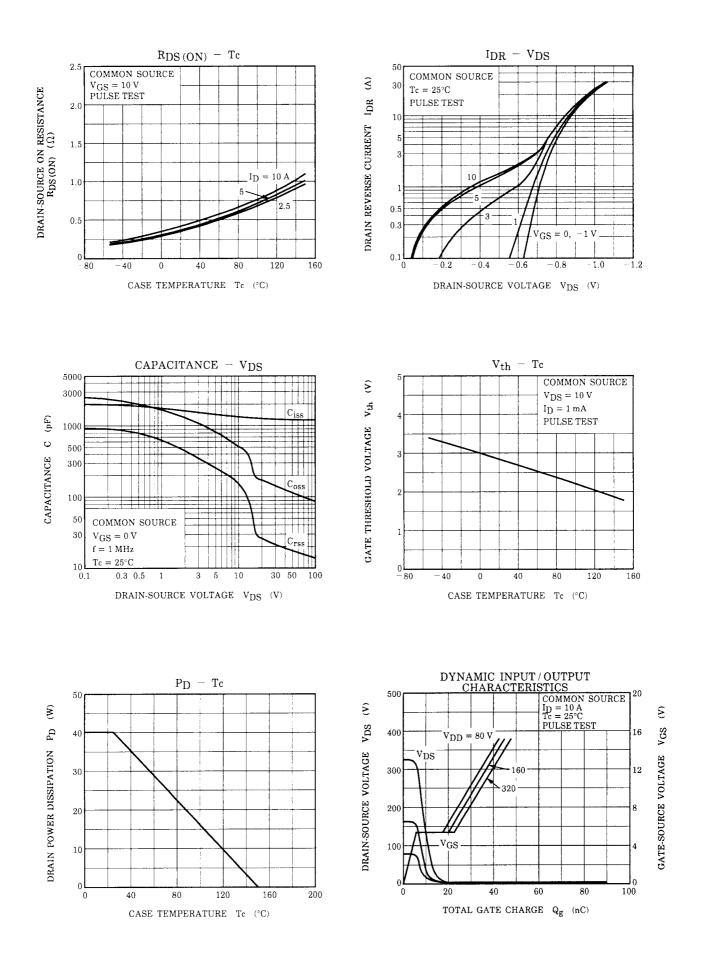
Marking

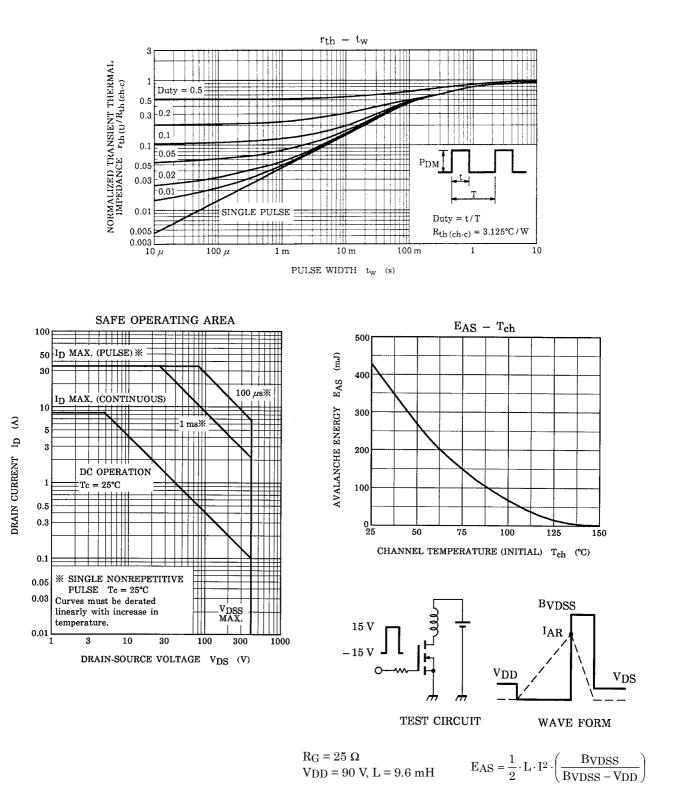


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